

## **APPLICATION DATA SHEET**

### **APPLICATION INFORMATION**

Filing Date:: 03/17/04  
Application Type:: Regular  
Subject Matter:: Utility  
Title:: Hard Phase Alignment Of Clock  
Signals With An Oscillator  
Controller  
Attorney Docket Number:: X-1203-1P US  
Request for Early Pub?:: No  
Request for Non-Pub?:: Yes  
Total Drawing Sheets:: 29  
Small Entity?:: No  
Petition included?:: No

### **APPLICANT INFORMATION**

Applicant Authority Type:: Inventor  
Primary Citizenship Ctry:: Canada  
Status:: Full Capacity  
Given Name:: Alireza  
Middle Name:: S.  
Family Name:: Kaviani  
Street:: 1803 Lencar Way  
City:: San Jose  
State or Province:: CA  
Postal or Zip Code:: 95124

### **CORRESPONDENCE INFORMATION**

Correspondence Customer Number:: 24309

**REPRESENTATIVE INFORMATION**

<b>Representative Customer Number::</b>	<b>24309</b>	
-----------------------------------------	--------------	--

**DOMESTIC PRIORITY INFORMATION**

<b>Application::</b>	<b>Continuity Type::</b>	<b>Parent Application::</b>	<b>Parent Filing Date::</b>
This Applic.	Continuation in Part	10/300,446	11/19/2002

**ASSIGNEE INFORMATION**

Assignee Name:: Xilinx, Inc.  
Street:: 2100 Logic Drive  
City:: San Jose  
State or Province:: California  
Postal or Zip Code:: 95124